

University of Mumbai

**1T01133**

Winter 2025

EXAMINATION TIME TABLE

PROGRAMME - S.E. (Electronics) (Choice Based) (R-19 'C' Scheme)

SEMESTER - III

Days and Dates	Time	Paper Code	Paper
Monday, 10 November, 2025	2:30 p.m. to 05:30 p.m.	51321	Engineering Mathematics-III
Wednesday, 12 November, 2025	2:30 p.m. to 05:30 p.m.	51322	Electronics Devices & Circuits I
Friday, 14 November, 2025	2:30 p.m. to 05:30 p.m.	51323	Digital Logic Circuits
Tuesday, 18 November, 2025	2:30 p.m. to 05:30 p.m.	51324	Electrical Networks Analysis & Synthesis
Thursday, 20 November, 2025	2:30 p.m. to 05:30 p.m.	51325	Electronic Instruments & Measurements

Important Note: •The candidates appearing for the examination should report 20 minutes before the start of examination.

- Mobile phones and other electronic devices are prohibited in the examination hall.
- Change if any, in the time table shall be communicated on the university web site.

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Mumbai - 400 098

26th July, 2025.

Dr. Pooja Raundale

Director

Board of Examinations & Evaluation