## University of Mumbai



1T01133

Winter 2024

## **EXAMINATION TIME TABLE**

## PROGRAMME - S.E. (Electronics) (Choice Based) (R-19 'C' Scheme)

SEMESTER - III

Days and Dates	Time	Paper Code	Paper
Tuesday, 12 November, 2024	2:30 p.m. to 05:30 p.m.	51321	Engineering Mathematics-III
Thursday, 14 November, 2024	2:30 p.m. to 05:30 p.m.	51322	Electronics Devices & Circuits I
Tuesday, 19 November, 2024	2:30 p.m. to 05:30 p.m.	51323	Digital Logic Circuits
Thursday, 21 November, 2024	2:30 p.m. to 05:30 p.m.	51324	Electrical Networks Analysis & Synthesis
Monday, 25 November, 2024	2:30 p.m. to 05:30 p.m.	51325	Electronic Instruments & Measurements

Important Note: The candidates appearing for the examination should report 20 minutes before the start of examination.

• Mobile phones and other eletronic gazets are prohibited in the examination hall.

• Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098 29<sup>th</sup> August, 2024. Dr.Pooja Raundale
Director
Board of Examinations & Evaluation