University of Mumbai



No. AAMS_UGS/ICC/2023-24/48

CIRCULAR:-

All the Engineering College Principals Affiliated to University of Mumbai are requested to authorize at least two nominations from your college as All India Council for Technical Education (AICTE) in association with Arm Education and ST Microelectronics with support from Cadence Design Systems is launching a Faculty Development Workshop on "VLSI to System Design: Silicon-to-End Application Approach" from 31st July, 2023 to 04th August, 2023. Aim of this workshop is to give insights to the faculty members on the latest technologies and developments prevailing in semiconductor related domains. The 5-day virtual workshop will take you through the VLSI SoC Silicon design steps to Industry 4.0 end –use applications.

The email dated 17th July, 2023 received from All India Council for Technical Education along with letter dated 06th July, 2023 is attached herewith, which is self-explanatory.

(The said circular is available on the University's website www.mu.ac.in).

MUMBAI – 400 032 28 ^{+h} July, 2023 (Sanjana Sawant)
Offg. Deputy Registrar
(AAMS Section)

To,

All the Engineering College Principals Affiliated to University of Mumbai.

Copy forwarded for information to:-

- 1) P.A. to the Vice Chancellor, University of Mumbai Fort, Mumbai -400 032.
- 2) P.A. to the Pro Vice Chancellor, University of Mumbai Fort, Mumbai -400 032.
- 3) P.A. to the Registrar, University of Mumbai Fort, Mumbai -400 032.



Virtual Faculty Development Workshop- VLSI to System design: Silicon-to-end application approach from 31st July to 4th August 2023.

All India Council for Technical Education(no-reply) <admin@aicte-india.org>
To: vice-chancellor@mu.ac.in

Mon, Jul 17, 2023 at 11:59 PM

Sir/Madam.

All India Council for Technical Education (AICTE) in association with **Arm Education** and **ST Microelectronics** with support from **Cadence Design Systems** is launching a Faculty Development Workshop on "VLSI to System Design: Silicon-to-End Application Approach" from 31st July to 4th August 2023. Aim of this workshop is to give insights to the faculty members on the latest technologies and developments prevailing in semiconductor related domains. The 5-day virtual workshop will take you through the VLSI SoC silicon design steps to Industry 4.0 end-use applications.

Please circulate this among all the faculty members and research scholars of Electronics and Communication Engineering, Electrical and Electronics Engineering, VLSI and Embedded Systems and authorize at least two nominations from each institute for the workshop. It is to be noted that there shall be **No Registration Charges**. The participants will be issued with a Certificate on successful completion of course.

For more information on the workshop – Registration, Agenda, Speakers, etc., please visit the link https://community.arm.com/vlsi-to-system-design/

Please feel free to contact the mentioned below officials for any related queries

Dr. Sunil Luthra, Director, Training and Learning Bureau, AICTE

E-mail: directortlb@aicte-india.org

Phone: 011-2958-1013

Ms. Apurva Varma, Senior Regional Manager-Academic Engagements, Arm Education

E-mail: apurva.varma@arm.com

PFA: https://drive.google.com/file/d/1TxFjEgdZnf_kJ7MS46hbzMsjO13ap81s/view?usp=sharing

Regards
Dr. Ramesh Unnikrishnan
Advisor
Training & Learning Bureau
All India Council for Technical Education
Email: advtlb@aicte-india.org

Phone: 011-2958-1215



Phone: 011-26131577 - 78, 80 011-29581000 Website: www.aicte-india.org



VC/700/2023-2412 अखिल भारतीय तकनीकी शिक्षा परिषद

(भारत संस्कार का एक मार्विधक निकाय) (शिक्षा मंत्रालय, चारत मरकार)

नेल्यान मंडेला पार्ग, यमंत कुंज, नई दिल्ली 110070

ALL INDIA COUNCIL FOR TECHNICAL EDUCATION

(A Statutory Body of the Govt of India) (Ministry of Education, Govt. of India) Nelson Mandela Marg, Vasant Kunj, New Delhi-110070

F. NO. 01/AICTE/TLB/ATAL/ARM EDU/2023

Date: 06.07.2023

To,

All Vice Chancellors of State Technical Universities, Directors / Principals of AICTE approved technical institutions,

Sub: Virtual Faculty Development Workshop- VLSI to System design: Silicon-to-end application approach from 31st July to 4th August 2023.

Sir/ Madam,

All India Council for Technical Education (AICTE) in association with Arm Education and ST Microelectronics with support from Cadence Design Systems is launching a Faculty Development Workshop on "VLSI to System Design: Silicon-to-End Application Approach" from 31st July to 4th August 2023. Aim of this workshop is to give insights to the faculty members on the latest technologies and developments prevailing in semiconductor related domains. The 5-day virtual workshop will take you through the VLSI SoC silicon design steps to Industry 4.0 end-use applications.

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Please feel free to contact the mentioned below officials for any related queries Dr. Sunil Luthra, Director, Training and Learning Bureau, AICTE E-mail: directortlb@aicte-india.org

Ms. Apurva Varma, Senior Regional Manager-Academic Engagements, Arm Education E-mail: apurva.varma@arm.com

DR-AAMS Circulate

Dr Ramesh Urba

Advisor

Training & Learning Bureau

18 JUL 2023