



T6932

SECOND HALF 2017

EXAMINATION TIME TABLE
PROGRAMME - M.E. (ELECTRONICS ENGG.) (Choice Based Credit & Grading System)
SEMESTER - II

Days and Dates	Time	Paper
Monday, November 20, 2017	11:00 a.m. to 02:00 p.m.	Digital Design with Reconfigurable Architecture
Wednesday, November 22, 2017	11:00 a.m. to 02:00 p.m.	Real Time System Design
Friday, November 24, 2017	11:00 a.m. to 02:00 p.m.	Advanced Signal Processing
Tuesday, November 28, 2017	11:00 a.m. to 02:00 p.m.	Elective I: Advanced Processor Architecture -II
	11:00 a.m. to 02:00 p.m.	Elective I: Wireless & Mobile Networking
	11:00 a.m. to 02:00 p.m.	Elective I: Nanoelectronics.
	11:00 a.m. to 02:00 p.m.	Elective I: Mechatronics.
	11:00 a.m. to 02:00 p.m.	Elective I: Virtual Instrumentation.
Thursday, November 30, 2017	11:00 a.m. to 02:00 p.m.	ElectiveII: Project Management.
	11:00 a.m. to 02:00 p.m.	ElectiveII: Finance Management.
	11:00 a.m. to 02:00 p.m.	ElectiveII: Entrepreneurship Development & Management.
	11:00 a.m. to 02:00 p.m.	ElectiveII: Human Resource Management.
	11:00 a.m. to 02:00 p.m.	ElectiveII: Professional Ethics & Corporate Social Responsibility.
	11:00 a.m. to 02:00 p.m.	ElectiveII: Research Methodology.
	11:00 a.m. to 02:00 p.m.	ElectiveII: IPR & Patenting.
	11:00 a.m. to 02:00 p.m.	ElectiveII: Digital Business Management.
	11:00 a.m. to 02:00 p.m.	ElectiveII: Environmental Management.

NOTE: The candidates appearing for the examination should report 15 minutes before the start of examination.
 Mobile phones and other electronic gazets are prohibited in the examination hall.
 Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098
 16th October, 2017.

(Prof. (Dr.) Arjun Ghatule)
 Offg. Director
 Board of Examinations & Evaluation