

**T6931**

FIRST HALF of (Summer) 2018

EXAMINATION TIME TABLE
PROGRAMME - M.E. (ELECTRONICS ENGG.) (Choice Based Credit & Grading System)
SEMESTER - I

Days and Dates	Time	Paper
Monday, May 14, 2018	11:00 a.m. to 02:00 p.m.	Advanced Digital Communication.
Wednesday, May 16, 2018	11:00 a.m. to 02:00 p.m.	Mixed Signal VLSI Design.
Friday, May 18, 2018	11:00 a.m. to 02:00 p.m.	Power Electronics System Design.
Tuesday, May 22, 2018	11:00 a.m. to 02:00 p.m.	Elective I: Advanced Processor Architecture -I
	11:00 a.m. to 02:00 p.m.	Elective I: Network & System Administration.
	11:00 a.m. to 02:00 p.m.	Elective I: Microelectronics Devices.
	11:00 a.m. to 02:00 p.m.	Elective I: Modeling & Simulations
	11:00 a.m. to 02:00 p.m.	Elective I: Advanced Digital Image Processing
Thursday, May 24, 2018	11:00 a.m. to 02:00 p.m.	Elective II: Product Lifecycle Management.
	11:00 a.m. to 02:00 p.m.	Elective II: Reliability Engineering.
	11:00 a.m. to 02:00 p.m.	Elective II: Management Information System.
	11:00 a.m. to 02:00 p.m.	Elective II: Design of Experiments.
	11:00 a.m. to 02:00 p.m.	Elective II: Operation Research.
	11:00 a.m. to 02:00 p.m.	Elective II: Cyber Security & Laws.
	11:00 a.m. to 02:00 p.m.	Elective II: Disaster Management & Mitigation Measures.
	11:00 a.m. to 02:00 p.m.	Elective II: Energy Audit & Management.

NOTE: The candidates appearing for the examination should report 15 minutes before the start of examination.

Mobile phones and other electronic gazets are prohibited in the examination hall.

Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098

17th April, 2018.

(Prof. (Dr.) Arjun Ghatule)

Offg. Director

Board of Examinations & Evaluation

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