



REVISED EXAMINATION TIME TABLE
PROGRAMME - M.E. (ELECTRONICS ENGG.) (Choice Based Credit & Grading System)
SEMESTER - I

Days and Dates	Time	Paper
Monday, December 04, 2017	11:00 a.m. to 02:00 p.m.	Advanced Digital Communication.
Thursday, December 07, 2017	11:00 a.m. to 02:00 p.m.	Mixed Signal VLSI Design.
Monday, December 11, 2017	11:00 a.m. to 02:00 p.m.	Power Electronics System Design.
Wednesday, December 13, 2017	11:00 a.m. to 02:00 p.m.	Elective I: Advanced Processor Architecture -I
	11:00 a.m. to 02:00 p.m.	Elective I: Network & System Administration.
	11:00 a.m. to 02:00 p.m.	Elective I: Microelectronics Devices.
	11:00 a.m. to 02:00 p.m.	Elective I: Modeling & Simulations
	11:00 a.m. to 02:00 p.m.	Elective I: Advanced Digital Image Processing
Friday, December 15, 2017	11:00 a.m. to 02:00 p.m.	Elective II: Product Lifecycle Management.
	11:00 a.m. to 02:00 p.m.	Elective II: Reliability Engineering.
	11:00 a.m. to 02:00 p.m.	Elective II: Management Information System.
	11:00 a.m. to 02:00 p.m.	Elective II: Design of Experiments.
	11:00 a.m. to 02:00 p.m.	Elective II: Operation Research.
	11:00 a.m. to 02:00 p.m.	Elective II: Cyber Security & Laws.
	11:00 a.m. to 02:00 p.m.	Elective II: Disaster Management & Mitigation Measures.
	11:00 a.m. to 02:00 p.m.	Elective II: Energy Audit & Management.

NOTE: The candidates appearing for the examination should report 15 minutes before the start of examination.

Mobile phones and other electronic gazets are prohibited in the examination hall.

Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098
 3rd November, 2017.

ST-295R

(Prof. (Dr.) Arjun Ghatule)
 Offg. Director
 Board of Examinations & Evaluation