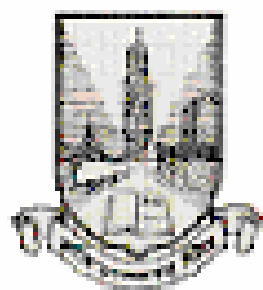


University of Mumbai



T6922

FIRST HALF 2017

**EXAMINATION TIME TABLE
PROGRAMME - M.E. (ELECTRONICS ENGG.)(CBSGS)
SEMESTER - II**

Days and Dates	Time	Paper
Wednesday, May 17, 2017	11:00 a.m. to 02:00 p.m.	Power Electronic Divices and Disign.
Friday, May 19, 2017	11:00 a.m. to 02:00 p.m.	Advanced Processor Architecture.
Tuesday, May 23, 2017	11:00 a.m. to 02:00 p.m.	Applications of DSP and IP.
Thursday, May 25, 2017	11:00 a.m. to 02:00 p.m.	Elective: III : Advance Networking Technologies.
	11:00 a.m. to 02:00 p.m.	Elective: III : Machine Learning.
	11:00 a.m. to 02:00 p.m.	Elective: III : Microwave IC.
	11:00 a.m. to 02:00 p.m.	Elective: III : Fabrication of Microelectronic IC.
Monday, May 29, 2017	11:00 a.m. to 02:00 p.m.	Elective: IV : Real Time Operating System.
	11:00 a.m. to 02:00 p.m.	Elective: IV : Modeling and Synthesis with VHDL.
	11:00 a.m. to 02:00 p.m.	Elective: IV : Cryptography and Network Securities.
	11:00 a.m. to 02:00 p.m.	Elective: IV : Application Specific IC Design.

NOTE: The candidates appearing for the examination should report 15 minutes before the start of examination.

Mobile phones and other elctronic gazets are prohibited in the examination hall.

Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098

16th March, 2017.

(DEEPAK G. WASAVE)

I/c. Director

Board of Examination & Evaluation