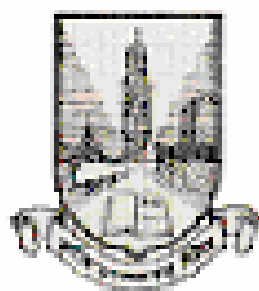


University of Mumbai



T6921

FIRST HALF 2017

EXAMINATION TIME TABLE PROGRAMME - M.E. (ELECTRONICS ENGG.) (CBSGS) SEMESTER - I

Days and Dates	Time	Paper
Tuesday, May 16, 2017	11:00 a.m. to 02:00 p.m.	System Modeling and Simulation.
Thursday, May 18, 2017	11:00 a.m. to 02:00 p.m.	Modelling of Microelectronic Devices.
Monday, May 22, 2017	11:00 a.m. to 02:00 p.m.	Embedded System.
Wednesday, May 24, 2017	11:00 a.m. to 02:00 p.m.	Elective : I. : Advance Digital Communication.
	11:00 a.m. to 02:00 p.m.	Elective : I. : Instrumentation System Design.
	11:00 a.m. to 02:00 p.m.	Elective : I. : Optical Fiber Communication.
	11:00 a.m. to 02:00 p.m.	Elective : I. : Analogue IC Design.
Friday, May 26, 2017	11:00 a.m. to 02:00 p.m.	Elective : II. : Wireless and Mobile Networks.
	11:00 a.m. to 02:00 p.m.	Elective : II. : Advance Digital Image Processing.
	11:00 a.m. to 02:00 p.m.	Elective : II. : Internetworking Technologies.
	11:00 a.m. to 02:00 p.m.	Elective : II. : Digital System Design.

NOTE: The candidates appearing for the examination should report 15 minutes before the start of examination.

Mobile phones and other electronic gadgets are prohibited in the examination hall.

Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098
16th March, 2017.

(DEEPAK G. WASAVE)
I/c. Director
Board of Examination & Evaluation