

E08REVCOL

UNIVERSITY OF MUMBAI Date : 01/11/2018
CENTRE WISE / COLLEGE WISE CANDIDATE LIST FOR Page : 1
THE B.E DEGREE (Electronics Engineering)(SEM VIII)(REVISED TO CBSGS) Examination NOVEMBER 2018

CENTRE/COLLEGE : 10 MUMBAI(MHSSCE)

SEAT NO	NAME OF CANDIDATE	CENTRE/COLLEGE	<--I-->			<--II-->			<--III-->			<--IV-->			<--V-->			<--VI-->			VII		VIII		PROJ.		APPL		
			WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	WR	TW	TH	TW	OP		
1.31	ADVANCE VLSI DESIGN																												
4.10	ADV. INSTRU. SYS. (SEM-6)																												
2.32	ROBOTICS & AUTOMATION																												
5.38	PROJECT-II																												
3.9	EMBEDDED SYS.DES. (SEM-7)																												
11180701	SHEIKH IMRAN SHEIKH GAFFAR ZAIRUN	10 MHSSCE	E	E	E	E	E	E	E	E	E	E	E	E	-	-	-	-	-	-	-	-	-	-	-	-	E	E	1

E08REVCOL

/ Female ; -- Not applicable ; E Exemption ; 0.5287 - APPEAR UNDER CLASS IMPROMENT. ⬆

UNIVERSITY OF MUMBAI
CENTRE WISE / COLLEGE WISE CANDIDATE LIST FOR
THE B.E DEGREE (Electronics Engineering)(SEM VIII)(REVISED TO CBSGS) Examination NOVEMBER 2018

Date : 01/11/2018
Page : 2

CENTRE/COLLEGE :237 VASHI (TEC)

SEAT NO	NAME OF CANDIDATE	CENTRE/COLLEGE	<--I-->			<--II-->			<--III-->			<--IV-->			<--V-->			<--VI-->			VII		VIII		PROJ.		APPL				
			WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	WR	TW	TH	OP	WR	OP			
1.1 4.13	CMOS VLSI DESIGN ASIC VERIFICATION (SEM-7)		2.4 5.38	ELECTIVE-II ROBOTICS PROJECT-II				3.9	EMBEDDED SYS.DES. (SEM-7)																						
11180702	DALVI ASHUTOSH VISHW AS NEELIMA	237 TEC		E	E		E	E		E	E		E	E		-	-	-	-	-	-	-	-	-	-	-	-	E	E		4
1.31 4.13	ADVANCE VLSI DESIGN ASIC VERIFICATION (SEM-7)		2.32 5.38	ROBOTICS & AUTOMATION PROJECT-II				3.9	EMBEDDED SYS.DES. (SEM-7)																						
11180703	/MHATRE SNEHALATA UDA Y JAYMALA	237 TEC		E	E	E	E	E	E	E	E		E	E		-	-	-	-	-	-	-	-	-	-	-	-	E	E		1
1.31 4.13	ADVANCE VLSI DESIGN ASIC VERIFICATION (SEM-7)		2.4 5.38	ELECTIVE-II ROBOTICS PROJECT-II				3.9	EMBEDDED SYS.DES. (SEM-7)																						
11180704	RATHORE ABHISHEK ABA DHESH SALONI	237 TEC		E	E	E	E	E		E	E		E	E		-	-	-	-	-	-	-	-	-	-	-	-	E	E		3

E08REVCOL

1.31 ADVANCE VLSI DESIGN 2.32 ROBOTICS & AUTOMATION 3.9 EMBEDDED SYS.DES. (SEM-7)
4.37 ELECTIVE-III ELECTRONIC PROD.DESIGN 5.38 PROJECT-II

11180705 MOTGHARE ROHAN RAJPA 237 TEC E E E E E E E E E E E - - - - - E E 2
 L ANITA

/ Female ; -- Not applicable ; E Exemption ; 0.5287 - APPEAR UNDER CLASS IMPROMENT. ^

UNIVERSITY OF MUMBAI
CENTRE WISE / COLLEGE WISE CANDIDATE LIST FOR
THE B.E DEGREE (Electronics Engineering)(SEM VIII)(REVISED TO CBSGS) Examination NOVEMBER 2018

Date : 01/11/2018
Page : 3

CENTRE/COLLEGE :385 VASHI (ACPCE)

SEAT NO	NAME OF CANDIDATE	CENTRE/COLLEGE	<--I-->			<--II-->			<--III-->			<--IV-->			<--V-->			<--VI-->			VII		VIII		PROJ.		APPL						
			WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	WR	TW	TH	TW	OP						
1.1 4.10	CMOS VLSI DESIGN ADV. INSTRU. SYS. (SEM-6)		2.4 ELECTIVE-II ROBOTICS			5.38 PROJECT-II			3.9 EMBEDDED SYS.DES. (SEM-7)																								
11180706	PASAD DARSHAN PRAVIN JAYSHREE	385 ACPCE	E	E		E	E		E	E		E	E		-	-		-	-		-	-		-	-		-	-		E	E		2
1.1 4.10	CMOS VLSI DESIGN ADV. INSTRU. SYS. (SEM-6)		2.32 ROBOTICS & AUTOMATION			5.38 PROJECT-II			3.9 EMBEDDED SYS.DES. (SEM-7)																								
11180707	SURVE AMANULLAH ABDU L QAYUM NOORJAHAN	385 ACPCE	E	E	E	E	E		E	E		E	E		-	-		-	-		-	-		-	-		-	-		E	E		1

E08REVCOL

/ Female ; -- Not applicable ; E Exemption ; 0.5287 - APPEAR UNDER CLASS IMPROMENT. ⬆

E08REVCOL

UNIVERSITY OF MUMBAI Date : 01/11/2018
CENTRE WISE / COLLEGE WISE CANDIDATE LIST FOR Page : 4
THE B.E DEGREE (Electronics Engineering)(SEM VIII)(REVISED TO CBSGS) Examination NOVEMBER 2018

CENTRE/COLLEGE :390 CHIPLUN(RSCE)

SEAT NO	NAME OF CANDIDATE	CENTRE/COLLEGE	<--I-->			<--II-->			<--III-->			<--IV-->			<--V-->			<--VI-->			VII		VIII		PROJ.		APPL	
			WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	WR	TW	TH	TW	OP	
1.1	CMOS VLSI DESIGN																											
4.34	ELECTIVE-III ADVANCED NETWORK.TECH.																											
2.2	ADVANCED NETWORK. TECH.																											
5.38	PROJECT-II																											
3.9	EMBEDDED SYS.DES. (SEM-7)																											
11180708	PATIL HRUDAYNATH RAM ESH SULOCHANA	390 RSCE	E	E	E	E	E	E	E	E	E	E	E	E	-	-	-	-	-	-	-	-	-	-	-	E	E	1

E08REVCOL

/ Female ; -- Not applicable ; E Exemption ; 0.5287 - APPEAR UNDER CLASS IMPROMENT. ⬆

UNIVERSITY OF MUMBAI
CENTRE WISE / COLLEGE WISE CANDIDATE LIST FOR
THE B.E DEGREE (Electronics Engineering)(SEM VIII)(REVISED TO CBSGS) Examination NOVEMBER 2018

Date : 01/11/2018
Page : 5

CENTRE/COLLEGE :442 VASHI (LTCE)

SEAT NO	NAME OF CANDIDATE	CENTRE/COLLEGE	<--I-->			<--II-->			<--III-->			<--IV-->			<--V-->			<--VI-->			VII		VIII		PROJ.		APPL			
			WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	WR	TW	TH	OP	WR	OP		
1.31 4.10	ADVANCE VLSI DESIGN ADV. INSTRU. SYS. (SEM-6)		2.32 5.38	ROBOTICS & AUTOMATION PROJECT-II						3.33	EMBED.SYS & RE.TIME PR.																			
11180709	CHAKURKAR SACHIN MAD HAV SUSHILA	442 LTCE		E	E	E	E	E	E	E	E	E	E	E	E	-	-	-	-	-	-	-	-	-	-	-	E	E	2	
1.1 4.10	CMOS VLSI DESIGN ADV. INSTRU. SYS. (SEM-6)		2.4 5.38	ELECTIVE-II ROBOTICS PROJECT-II						3.9	EMBEDDED SYS.DES. (SEM-7)																			
11180710	KHAN ABUZAR FAIZAAN AHMED ATIKA	442 LTCE		E	E		E	E		E	E	E	E	E	-	-	-	-	-	-	-	-	-	-	-	-	E	E	3	
1.31 4.34	ADVANCE VLSI DESIGN ELECTIVE-III ADVANCED NETWORK.TECH.		2.32 5.38	ROBOTICS & AUTOMATION PROJECT-II						3.9	EMBEDDED SYS.DES. (SEM-7)																			
11180711	GHADIGAONKAR KAMLESH SANTOSH SWATI	442 LTCE		E	E	E	E	E	E		E	E	E	E	E	-	-	-	-	-	-	-	-	-	-	-	E	E	1	

E08REVCOL

11180712 SAKPAL SUSHIL SUBHAS 442 LTCE E E E E E E E E E E E - - - - - E E 3
H SHUBHANGI

/ Female ; -- Not applicable ; E Exemption ; 0.5287 - APPEAR UNDER CLASS IMPROMENT. ⬆

E08REVCOL

UNIVERSITY OF MUMBAI Date : 01/11/2018
CENTRE WISE / COLLEGE WISE CANDIDATE LIST FOR Page : 6
THE B.E DEGREE (Electronics Engineering)(SEM VIII)(REVISED TO CBSGS) Examination NOVEMBER 2018

CENTRE/COLLEGE :561 PANVEL(PIITE)

SEAT NO	NAME OF CANDIDATE	CENTRE/COLLEGE	<--I-->			<--II-->			<--III-->			<--IV-->			<--V-->			<--VI-->		VII	VIII	PROJ.	APPL			
			WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW					TH	OP	
1.1	CMOS VLSI DESIGN																									
4.10	ADV. INSTRU. SYS. (SEM-6)																									
2.32	ROBOTICS & AUTOMATION																									
5.38	PROJECT-II																									
3.33	EMBED.SYS & RE.TIME PR.																									
11180713	KAUL MOHIT MOHANLAL MOHANA	561 PIITE	E	E	E	E	E	E	E	E	E	E	E	E	-	-	-	-	-	-	-	-	-	E	E	1

E08REVCOL

/ Female ; -- Not applicable ; E Exemption ; 0.5287 - APPEAR UNDER CLASS IMPROMENT. ⬆

E08REVCOL

UNIVERSITY OF MUMBAI Date : 01/11/2018
CENTRE WISE / COLLEGE WISE CANDIDATE LIST FOR Page : 7
THE B.E DEGREE (Electronics Engineering)(SEM VIII)(REVISED TO CBSGS) Examination NOVEMBER 2018

CENTRE/COLLEGE :689 THANE(KCCE)

SEAT NO	NAME OF CANDIDATE	CENTRE/COLLEGE	<--I-->			<--II-->			<--III-->			<--IV-->			<--V-->			<--VI-->			VII		VIII		PROJ.		APPL	
			WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	OP	WR	TW	WR	TW	TH	TW	OP	
1.1	CMOS VLSI DESIGN																											
2.32	ROBOTICS & AUTOMATION																											
3.33	EMBED.SYS & RE.TIME PR.																											
4.34	ELECTIVE-III ADVANCED NETWORK.TECH.																											
5.38	PROJECT-II																											
11180714	BAJAJ ASHISH NARESH NEETA	689 KCCE	E	E	E	E	E	E	E	E	E	E	E	E	-	-	-	-	-	-	-	-	-	-	-	E	E	1

/ Female ; -- Not applicable ; E Exemption ; EL-Elective ; 0.5287 - APPEAR UNDER CLASS IMPROMENT. ⬆