

University of Mumbai



(302)
SECOND HALF - 2007

PROGRAMME OF THE B.E.(ELECTRONICS) (SEM VIII) (REV) EXAMINATION.

Candidates for the above examination are requested to be in attendance at the place of examination, fifteen minutes before the time appointed for setting of the first paper and ten minutes before the time fixed for setting of each subsequent paper.

THEY ARE FORBIDDEN TO TAKE ANY BOOK OR PAPER INTO THE EXAMINATION HALL.

Seat numbers and places of examination will be announced on the college notice boards four days prior to the date of commencement of the examination.

Smoking is strictly prohibited in the examination hall.

The written examination will be conducted in the following order: -

Days and Dates	Time	Paper
Tuesday, 4 th December, 2007	10:30 AM to 01:30 PM	Power Electronics.
Monday, 10 th December, 2007	10:30 AM to 01:30 PM	Data Communication & Networking.
Friday, 14 th December, 2007	10:30 AM to 01:30 PM	Mechatronics.
Wednesday, 19 th December, 2007	10:30 AM to 01:30 PM	Elective: II. : Advanced Digital Signal Processing.
		Elective: II. : Biomedical Instrumentation.
		Elective: II.: Robotics.
		Elective: II. : Embedded Systems & Real Time Programming.
		Elective: II. : Telecom Network Management.
		Elective: II. : VLSI Design.

Candidates are permitted to use stencils at the examination.

Candidates for the examination are expected to provide themselves with slide-rules.

The exchange or loan of slide rules, drawing instruments or other materials used in the examination is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances.

Mumbai – 400 098
3rd November, 2007

PRAKASH J. WANI
O.S.D. & CONTROLLER OF EXAMINATIONS