

University of Mumbai



(259)
Second Half - 2008

PROGRAMME OF THE T.E. (ELECTRONICS)(SEM. VI)(OLD) EXAMINATION.

Candidates for the above examination are requested to be in attendance at the place of examination, fifteen minutes before the time appointed for setting of the first paper and ten minutes before the time fixed for setting of each subsequent paper.

THEY ARE FORBIDDEN TO TAKE ANY BOOK OR PAPER INTO THE EXAMINATION HALL.

Seat numbers and places of examination will be announced on the college notice boards four days prior to the date of commencement of the examination.

Smoking is strictly prohibited in the examination hall.

The written examination will be conducted in the following order: -

Days and Dates	Time	Paper
Monday, 1 st December, 2008	02:30 PM to 05:30 PM	Microprocessor – II
Monday, 8 th December, 2008	02:30 PM to 05:30 PM	Designing with Integrated Circuits
Saturday, 13 th December, 2008	02:30 PM to 05:30 PM	Satellite T.V. and Video Technology
Thursday, 18 th December, 2008	02:30 PM to 05:30 PM	Industrial Electronics
Tuesday, 23 rd December, 2008	02:30 PM to 05:30 PM	Principles of Economics and Management
Tuesday, 30 th December, 2008	02:30 PM to 05:30 PM	Analog Integrated Circuits

Candidates are permitted to use stencils at the examination.

Candidates for the examination are expected to provide themselves with slide-rules.

The exchange or loan of slide rules, drawing instruments or other materials used in the examination is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances.

Mumbai – 400 098

PROF. VILAS B. SHINDE
CONTROLLER OF EXAMINATIONS